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[SPS90198 - HF680BT : hf680bt-kernel]

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Copyright message. * 0.0.19 * Added I2C and SPI registers. Filled in interrupt enable. * 0.0.20 * Added GPIO info for SB Live 24bit. * 0.0.21 * Implement support for Line-in capture o

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Copyright (C) 2008 Igor M. Liplianin < liplianin@me.by> Removed stb6000 specific tuner code and revised some procedures. 2010-09-01 Josef Pavlik < josef@pavlik.it>

Fixed diseqc_msg, diseqc_burst

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disp_iovec(mic, copy, __func__, __LINE__);

mpsslog("%s %s %d wrote to net 0x%lx\n",

mic->name, __func__, __LINE__,

sum_iovec_len(copy));#endif

/* Reinitiali

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code to create this file * Oct 23 2003 kaos@sg

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```
COPYRIGHT 0x01#define DVD_STRUCT_DISCKEY 0x02#define DVD_STRUCT_BCA 0x03#define DVD_STRUCT_MANUFACT 0x04

COPYRIGHT); } return pci register driver(}
```

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Copyright (C) 2013 Tom Gundersen <teg@jklm.no> * * This code registers /sys/firmware/efi{,/efivars} when EFI is supported, * allowing the efivarfs to be mounted or the efivars module to be loaded.

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Copyright unasserted) * AN = 0 (Audio data) * P = 0 (Consumer) */ snd_emu10k1x_ptr_write(chip, SPCS0, 0, chip->spdif_bits[0] = SPCS

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Copyright unasserted) * AN = 0 (Audio data) * P
(Consumer) */ emu->spdif_bits[0] = emu->spdif_bits[1] =

emu->spdif_bits[2] = SPCS_CLKACCY_1000PPM

Copyright (C) 1997 Geert Uytterhoeven * Based on linux/driver/video/pm2fb.c:

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= 0

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ucontrol->value.iec958.status[1] = IEC958_AES1_CON_ORIGINAL |

IEC958_AES1_CON_CATEGORY; ucontrol->value.iec958.status[3]

=

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 \pm Based on former asm-ppc/div64.h and asm-m68knommu/div64.h * The semantics of do_div() are: *

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disp_iovec(mic, copy, __func__,

__LINE__); mpsslog("%s %s %d ",

mic->name, __func__, __LINE__);

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mpsslog("wrote to tap 0x%lx\

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Copyright © 2002, 2003 David Dawes <dawes@xfree86.org> * 2004 Sylvain Meyer * * This driver consists of two parts. The first part (intelfbdrv.c) provides * the basic

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Copyright (C) 2011 Netlogic Microsystems. */#ifndef __ASM_NETLOGIC_IRQ_H#define __ASM_NETLOGIC_IRQ_H

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©)) { mpsslog("Tun write failed

strerror(errno));

mpsslog("len 0x%zx ", len);

mpsslog("read_len

0x%zx\n'',

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Copyright (C) 1995, 1996 by Andreas Busse * * NOTE: Some of the argument checking could be removed when * things have settled down. Also, instead of returning 0xffffffff * on failure of vdma_alloc

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copyright bit for channel status * [4:2] set additional information for channel status * [1:0] set clock accuracy for channel status */ hdmi_write(hdmi, 0x00, HDMI AUDIO STATUS BITS SETTING 2);

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copyright(struct cdrom_device_info

struct packet_command *cgc){

int ret; u_char buf[8]; struct cdrom_device_ops *cdo = cdi->ops;

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IEC958_AES4_CON_ORIGFS_NOTID | IEC958_AES4_CON_MAX_WORDLEN_24;

reg_write_range(priv, REG_CH_STAT_B(0), buf, 4);

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DEBUGmodule_param(debug_msgs, bint, 0);MODULE_PARM_DESC(debug_msgs, "Enable

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(70)#define EEPROM_SERIAL

(112)#defin

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vnet_hdr[1];

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"%s %d errno ret %d\n", __func__, __LINE__, ret); return

ret; } if (copy_to_user(&((struct mic_copy_desc __user
*)argp)->out_len,

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David Mosberger-Tang <davidm@hpl.hp.com> * * 6/10/99: Updated to bring in sync with x86 version to facilitate

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```
COPYRIGHT)) sbits if (val & AC_DIG1_LEVEL)
```

 $sbits \models (IEC958_AES1_CON_ORIGINAL << 8); \qquad sbits \models val \& (0x7f << 8); \\$

return sbits;

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```

val |= AC_DIG1_LEVEL;

val |= sbits & (IEC958_AES1_CON_CATEGORY << 8); }

return val;

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linux/ethtool.h>#include <linux/slab.h>#include <linux/pci.h>#include <l</pre>

Copyright (c) 2007-2008, Realsil Wlan\n"); RT_TRACE(COMP_INIT, "Initializing module"); RT_TRACE(COMP_INIT, "Wireless extensions version %d", WIRELESS_EXT); rtl8192_proc_module_init(); return usb_r

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&copy, \_func\_\_, \_LINE\_\_); mpsslog("%s %s %d read failed %s ",

mic->name, \_\_func\_\_, \_\_LINE\_\_,

strerror(errno)); mpsslog("cnt %d sum %zd\n",

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©[i continue; } merge_bio.head =

copy[i].request->bio; merge_bio.tail = copy[i].request->biotail;

bio_list_merge(&bio_list, &merge_bio); copy[i].request->bio = NULL;

blk end request

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©, _func_, _LINE_); mpsslog("%s %s %d read failed %s ", mic->name, __func_, _LINE_, strerror(errno));

mpsslog("cnt %d sum %zd\n",

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ucontrol->value.iec958.status[1] = IEC958_AES1_CON_ORIGINAL |

IEC958_AES1_CON_CATEGORY; ucontrol->value.iec958.status[3]

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linux/hardirq.h>#include linux/highmem.h>#include linux/kernel.h>#include
linux/li

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Copyright (c) 1998-2001 Axis Communications AB \* \* csum\_partial(const unsigned char \* buff, int len, unsigned int sum)

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&copy, &rs->rs\_notify\_queue); spin\_unlock\_irqrestore(&rs->rs\_lock, flags);

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Copyright (c) 2009-2010 Analog Devices, Inc. \* Author: Michael Hennerich <a href="https://www.enerich@blackfin.uclinux.org">hennerich@blackfin.uclinux.org</a> \* DEVID #Wipers #Positions Resistor Options (kOhm) \* AD5258 1 64 1, 10, 50, 100 \*

Copyright (C) 1994-2003 Dario Ballabio.\n"); printk ("%s config options -> tm:%d, Ic:%c, mq:%d, rs:%c, et:%c, " "ip:%c, ep:%c, pp:%c.\n", driver\_name, tag\_mode,

YESNO(linked\_comm

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&copy, argp, sizeof(copy))) err = -EFAULT; goto

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(1<<5)#define

AC\_DIG1\_PROFESSIONAL

(1<<6)#define AC\_DIG1\_LEVEL

(1 << 7)

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&copy->hc hai.hai fid);

if (IS ERR(inode)) {

hpk.hpk\_flags |=

HP\_FLAG\_RETRY;

/\* hpk\_errval must be >= 0 \*/

hpk.hpk\_errval

```
= -PTR_ERR(inode); rc = PTR_ERR(inode); goto progress; }
```

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COPYRIGHT; switch (bits case 0x10: diga->status[0] |= IEC958\_AES0\_CON\_EMPHASIS\_NONE; break; case 0x00: diga->status[0] |= IEC958\_AES0\_CON\_EMPHASIS\_5015; break; } if (!(bits & 0x80))

diga-

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chip->spdif\_bits[1] = SPCS\_CLKACCY\_1000PPM | SPCS\_SAMPLERATE\_48 | SPCS\_CHANNELNUM\_LEFT | SPCS\_SOURCENUM\_UNSPEC |

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                                                                        start merging tpconfig and
gpm code to a xfree-input module *
                                       adding some changes and extensions (ex. 3rd and 4th button)
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```

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/\* Receive is empty \*/#define BD\_SC\_READY ((ushort)0x8000) /\* Transmit is ready \*/#define BD\_SC\_WRAP((ushort)

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&copy));#endif /\* Reinitialize IOV for next run \*/ iov0[1].iov len = MAX NET PKT SIZE;

ellet else if (len < 0) {

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&copy, argp, sizeof(copy)))

err = -EFAULT:

anto

writeto\_err; }

err = scif\_writeto(priv, copy.loffset, copy.len, copy.roffset,

copy.flags);writeto\_err:

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&copy);#ifdef DEBUG disp\_iovec(mic, copy, \_\_func\_\_, \_\_LINE\_\_); mpsslog("%s %s %d ", mic->name, \_\_func\_\_, \_\_LINE\_\_);

mpsslog("read from net 0x%lx\n",

sum\_iovec\_len(

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IEC958\_AES1\_CON\_ORIGINAL; ucontrol->value.iec958.status[3] = IEC958\_AES3\_CON\_FS; return 0;}

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if ((status[0] & IEC958 AESO CON EMPHASIS) != IEC958 AE

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copyright + flash\_ver images, 0 for failure.static u32 chk\_cfg(u8 \*cfg, u32 length, u32 \*flash\_ver){  $u16 *pw = (u16 *)cfg - 1; u32 sz = 0; u32 len = length;}$ 

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Copyright unasserted) * AN = 0 (Audio data) * P
= 0 (Consumer) */ def_bits = SPCS_CLKACCY_1000PPM |
SPCS_SAMPLERATE_48 | SPCS_CHANNELNUM_LEFT | SPCS_

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nf

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