DIGITATIONS FORGESENSORS 12C COMMUNICATIONS

Technical Note

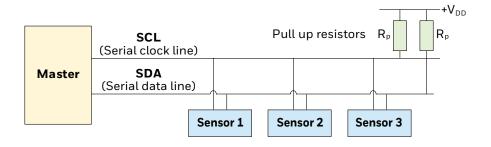
This Technical Note provides implementation information for Honeywell force sensors with I²C (Inter-IC) digital output.

1.0 INTRODUCTION

The I^2C (Inter-IC) bus is a simple, serial 8-bit oriented computer bus for efficient I^2C (Inter-IC) control. It provides good support for communication between different ICs across short circuit-board distances, such as interfacing microcontrollers with various low speed peripheral devices.

Each device connected to the bus is software addressable by a unique address and a simple "Master"/"Sensor" relationship that exists at all times. The output stages of devices connected to the bus are designed around an open collector architecture. Because of this, pull-up resistors to +V_DD must be provided on the bus. Both SDA (Serial Data Line) and SCL (Serial Clock Line) are bidirectional lines, and it is important for system performance to match the capacitive loads on both lines. In addition, in accordance with the I²C specification, the maximum allowable capacitance on either line is 400 pF to ensure reliable edge transitions at 400 kHz clock speeds (see Figure 1). When the bus is free, both lines are pulled up to +V_DD. Data on the I²C-bus can be transferred at a rate up to 100 kbit/s in the standard mode, or up to 400 kbit/s in the fast mode.

FIGURE 1. I²C BUS CONFIGURATION



NOTICE

For detailed specifications of the I²C protocol, see Version 2.1 (January 2000) of the I²C Bus Specification (source: NXP Semiconductor at (http://www.nxp.com/acrobat_download/literature/9398/39340011.pdf).

2.0 DATA TRANSFER

Honeywell's digital output force sensors are designed to respond to requests from a Master device. Following the address and read bit from the Master, these Sensors are designed to output up to four bytes of data, depending on the sensor options and application needs. In all cases, the first two data bytes are the compensated force output, along with sensor status bits. The third and fourth bytes are for optional compensated temperature output.

2.1 SENSOR ADDRESS

Each sensor is referenced on the bus by a 7-bit Sensor address. The default address for Honeywell force sensors is 40 (28 hex). Other available standard addresses are: 56 (38 hex), 72 (48 hex), 88 (58 hex), 104 (68 hex), 120 (78 hex). (Other custom values are available. Please contact Honeywell Customer Service.)



FORCE READING (SEE FIGURE 2)

The Master generates a "Start" condition and sends the Sensor address followed by a "Read" bit. After the Sensor generates an "Acknowledge" (ACK), it will transmit up to four bytes of data: the first two bytes contain the compensated force output, and the second two bytes contain the optional compensated temperature output. The Master must acknowledge the receipt of each byte,

and can terminate the communication by sending a "Not Acknowledge" (NACK) bit followed by a "Stop" bit after receiving both bytes of data.

FIGURE 2. I²C FORCE AND TEMPERATURE MEASUREMENT PACKETS READOUT¹

Two byte data readout

												D	ata I	oy te	1						D	ata k	oy te	2				
Start	A6	A5	Α4	А3	A2	A1	Α0	1	ACK	S1	SO	B13	B12	B11	B10	В9	B8	ACK	В7	В6	B5	B4	В3	B2	B1	В0	NACK	Stop
		Se	nsor	addr	ess [6.0]		Read		Sta	tus		Brid	geda	ta[1	3.8]					Brid	lg e d	ata[7.0]				

Three byte data readout

												D	ata I	oy te	1						D	ata I	yte	2						D	ata k	yte	3				
Start	A6	A5	Α4	А3	A2	A1	A0	1	ACK	S1	S0	B13	B12	B11	B10	В9	B8	ACK	В7	В6	B5	В4	В3	B2	B1	В0	ACK	T10	Т9	T8	T7	T6	T5	T4	Т3	NACK	Stop
		Se	nsor	addr	ess [6.0]		Read		Sta	tus		Brid	g e da	ta[1	.3.8]					Brio	lg e d	ata[7.0]						Brio	lg e d	ata[:	10.3]			

Four byte data readout

												Da	ta by	te 1						Da	ata b	y te	2						Data	byte	3						D	ata k	yte 4	4			
Start	A6	A5	Α4	А3	A2	A1	A0	1	ACK	S1	SO	B13 E	312 B.		В9	B8	ACK	В7	Kh I	B5	В4	В3	B2	B1	B0	ACK	T10 1	9 T8	T7	T6	T5	T4	T3	ACK	T2	T1	TO	χ	Х	χ	χ	χ	NACK Stop
		Se	nsor	addı	ress	[6.0]	ĺ	Read	t	Sta	tus	E	lridg e	da ta [.	[3.8]					Brid	ged	ata[7.0]					Br	dge	da ta [10.3]					Bric	lg e d	ata[2	2.0]			

Bits generated by Master Bits generated by Sensor

2.3 **TEMPERATURE READING**

The optional corrected temperature data may be read out with either 8-bit or 11-bit output. See Table 1 for specifics.

TABLE 1. OPT	IONAL COMPENSATED TEMPERATURE		
OUTPUT	READING	LOCATION	RESOLUTION
8-bit	Optional compensated temperature value	third data byte	0.8°C
11-bit	Complete optional compensated temperature value	fourth data byte ¹	0.1°C

 $^{^{}m 1}$ The five least significant bits of the fourth data byte are "Do Not Care" and should be ignored.

2.4 **DIAGNOSTIC STATES**

Honeywell digital output force sensors offer both standard and optional diagnostics to ensure robust system operation in critical applications. The diagnostic state is indicated by the first two Most Significant Bits of data byte 1 as shown in Table 2.

		9 9
TABLE	2. DIAGI	NOSTIC STATES
STATL	JS BITS	DEFINITION
S1	S0	DEFINITION
0	0	Normal operation, valid data
0	1	Device in command mode ¹
1	0	Stale data: Indicates data that has already been fetched since the last measurement cycle, or data fetched before the first measurement has been completed
1	1	Diagnostic condition

 $^{^{}m 1}$ Command mode is used for programming the sensor. This mode should not be seen during normal operation.

A status bit reading of "10" indicates "stale" data. This state may occur when the Master polls the data quicker than the sensor can update the output buffer.

2.41 **Standard Diagnostic Function**

The standard diagnostic function is an EEPROM (Electrically Erasable Programmable Read-Only Memory) signature used to validate the EEPROM content during startup. Any EEPROM content change after calibration flags a diagnostic condition.

2.42 **Optional Diagnostic Functions**

The two optional diagnostic functions, either of which may be indicated by a status bit reading of "11", are:

- Loss of sense element connection
- · Short circuit of sense element

(Please contact Honeywell Customer Service with questions regarding the availability of optional digital output force diagnostics.)

3.0 CALCULATING FORCE FROM THE DIGITAL OUTPUT

The output of the sensor may be expressed by its transfer function as shown in Equation 1.

Equation 1: Force Sensor Transfer Function

$$Output = \frac{(Output_{max} - Output_{min})}{Rated Force Range} \times (Force_{applied}) + Output_{min}$$

Rearranging this equation to solve for force provides Equation 2:

Equation 2: Force Output Function

Force =
$$\frac{(Output - Output_{min.})}{(Output_{max} - Output_{min})} \times Rated Force Range$$

Where:

Output_{max.} = output at maximum force [counts]

Output_{min.} = output at minimum force [counts]

Force_{rated} = maximum value of force range (N, lb, g, or kg)

Force_{applied} = Force being measured by the sensor (N, lb, g, or kg)

Output = digital force reading [counts]

Example: Calculate the force for a 10 N force sensor with a 10% to 90% calibration and a force output of 6880 (decimal) counts:

Output_{max.} = 14745 counts (90% of 2^{14} counts or 0x3999)

Output_{min.} = 1638 counts (10% of 2^{14} counts or 0x0666)

Force_{rated} = 10 N

Force = force in N

Output = 6880 counts

Force = $[(6880 - 1638) \times 10]$ (14745 - 1638)

Force = (5242×10)

Force = 4 N

4.0 CALCULATING OPTIONAL TEMPERATURE FROM THE **DIGITAL OUTPUT**

For those sensors so equipped, the optional compensated temperature output may be converted to °C using Equation 3:

Equation 3: Temperature Conversion Function

Temperature (°C) =
$$\left(\frac{\text{Output (dec)}}{2047} \times 200\right)$$
 - 50

If the 8-bit temperature output is used, shift the data to the left by three bits and set the three Least Significant Bits (LSB) to zeros.

Example: Calculate the optional compensated temperature output for a sensor with an 8-bit temperature output of 255:

Step 1: Left shift the above 8-bit value by three places and append the three LSBs with zeros:

Digital Temperature Output (8-bit) = 255 = 11111111b

11111111000b = 2040

Step 2: Use the adjusted value and insert into Equation 3:

Temperature (°C) =
$$\left(\frac{2040}{2047} \times 200\right)$$
 - 50

Temperature = 149.31°C

Example: Calculate the optional compensated temperature for a sensor with an 11-bit temperature output of 1456:

Step 1: Insert the digital temperature output value into Equation 3:

Temperature (°C) =
$$\left(\frac{1456}{2047} \times 200\right)$$
 - 50

Temperature = 92.26°C

5.0 TIMING AND LEVEL PARAMETERS (SEE FIGURE 3 AND TABLE 3)

FIGURE 3. TIMING AND LEVEL PARAMETERS

11	٧o	by	te	da	ta	rea	ado	out																			
												D	ata I	by te	1						D	ata b	yte	2			
Start	A6	A5	Α4	А3	A2	A1	A0	1	ACK	S1	SO	B13	B12	B11	B10	B9	B8	ACK	В7	B6	B5	В4	В3	B2	B1	В0	NACK
		Se	nsor	addr	ess [6.0]		Read		Sta	tus		Brid	geda	ta[1	3.8]					Brio	lged	ata[7.0]			

Three byte data readout

												D	ata I	byte	1						D	ata t	oy te	2						D	ata I	yte	3				
Start	A6	A5	Α4	А3	A2	A1	A0	1	ACK	S1	SO	B13	B12	B11	B10	B9	B8	ACK.	B7	B6	B5	B4	В3	B2	B1	В0	VCK	T10	T9	T8	T7	T6	T5	T4	Т3	NACK	Stop
		Se	nsor	addr	ess	[6.0]		Read		Sta	itus		Brid	geda	ata[1	.3.8]					Brio	lged	ata[7.0]						Bric	lg e d	ata[10.3]			

Four byte data readout

	uui		, ce	u	ata		au	ou																																							
													Da	ta b	y te	1						D	ata I	by te	2]			D	ata I	byte	3						D	ata l	byte	4				
Start	A6	A5	Α4	A3	A2	A1	A0	1	ACK	S1	SI	В	13	B12	B11	B10	В9	В8	ACK	В7	B6	B5	В4	В3	B2	B1	В0	ACK	T10	T9	T8	T7	T6	T5	T4	Т3	ACK.	T2	T1	TO	Х	χ	Х	χ	X		Stop
		Se	ensor	add	ress	6.0]	Rea	d	Sta	atus	s	E	Bridg	jeda	ta[1	.3.8					Brio	dged	lata	[7.0]						Brio	dged	lata[10.3]					Brio	dged	ata[2.0]			Г	Т

TABLE 3. TIMING AND LI	EVEL PARA	METERS			
PARAMETER	SYMBOL	MINIMUM	TYPICAL	MAXIMUM	UNIT
SCL clock frequency	f _{SCL}	100	_	400	kHz
Start condition hold time relative to SCL edge	t _{HDSTA}	0.1	_	_	μs
Minimum SCL clock width ¹ : low high	t _{LOW} t _{HIGH}	0.6 0.6		_ _	μs μs
Start condition setup time relative to SCL edge	t _{SUSTA}	0.1	_	_	μs
Data hold time on SDA relative to SCL edge	t _{HDDAT}	0	_	_	μs
Data set-up time on SDA relative to SCL edge	t _{SUDAT}	0.1	_	_	μs
Stop condition setup time on SCL	t _{SUSTO}	0.1	_	_	μs
Bus free time between stop condition and start condition	t _{BUS}	2	_	_	μs
Output level: low high	Out _{LOW} Out _{HIGH}	_ 0.8	O 1	0.2	V_{DD} V_{DD}
Pull-up resistance on SDA and SCL	R_P	1	_	50	kOhm

¹Combined low and high widths must equal or exceed minimum SCL period.

FOR MORE INFORMATION

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USA/Canada +1 302 613 4491 Latin America +1 305 805 8188 Europe +44 1344 238258 Japan +81 (0) 3-6730-7152 Singapore +65 6355 2828 Greater China +86 4006396841

Honeywell

Sensing and Internet of Things

830 East Arapaho Road Richardson, TX 75081 sensing.honeywell.com

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