## Honeywell

# Installation Instructions for the **Back-Bias Hall-effect Speed Sensor: VG481V1**

#### **GENERAL INFORMATION**

The VG481V1 contains a dual peak detecting circuit that is coupled with a Hall-effect element to detect the speed of a ferrous gear tooth target wheel. A biasing magnet provides a changing magnetic field as the target wheel moves past the senor IC. The sensor IC functions in reference to peak and slot magnetic levels. The sensor IC output is in either a high state or a low state, which corresponds to the target tooth/slot features.

#### **CAUTION** ELECTROSTATIC DISCHARGE DAMAGE

Ensure proper ESD precautions are followed when handling this product.

Failure to comply with these instructions may result in product damage.



#### SOLDERING AND ASSEMBLY

#### CAUTION

#### IMPROPER SOLDERING

- Ensure leads are adequately supported during any forming/ shearing operation so that they are not stressed inside the plastic case.
- Limit exposure to high temperatures.

### Failure to comply with these instructions may result in product damage.

Wave solder at 250°C to 260°C [482°F to 500°F] for a maximum of three seconds. Burrs are allowed only if full lead length will pass through a 0,68 mm [0.027 in] dia. hole.

#### CLEANING

#### CAUTION IMPROPER CLEANING

Do not use pressure wash. High-pressure stream could force contaminants into the package. **Failure to comply with these instructions may result in** 

product damage.

Use agitated rinse to clean the sensor.

TABLE 1. ELECTRICAL SPECIFICATIONS (AT 4.0 V $\leq$ V <sub>S</sub> $\leq$ 24 V, -40°C $\leq$ T <sub>A</sub> $\leq$ 150°C, UNLESS OTHERWISE SPECIFIED)					
CHARACTERISTIC	CONDITION	MIN.	TYP.	MAX.	UNIT
Supply voltage	Pin 2 referenced to Pin 1	4	14	24	V
Reverse supply voltage	current < 10 mA	_	_	-26	V
Supply current	V <sub>supply</sub> = 24 V, with "ON" state	_	-	10	mA
Startup time	$V_{supply} = 4 V$	-	-	10	ms
Output short circuit	short load	-	10	-	μs
Output leakage current	$V_{supply}$ = 24 V, with "OFF" state	-	-	10	μΑ
Output current (sinking)	continuous operation (recommended)	-	-	5	mA
Output saturation voltage	sinking 20 mA	-	-	360	mV
Switching frequency: rise time fall time	$V_{cc}$ = 12 V, R <sub>L</sub> =2 kOhm; C <sub>L</sub> = 20 pF V <sub>cc</sub> = 12 V, R <sub>L</sub> =2 kOhm; C <sub>L</sub> = 20 pF			10 1	μs μs
Power up ramp time <sup>1</sup>	0 V to 4 V, power up in high state			1	ms

<sup>1</sup>If the power up ramp time from 0 V to 4 V is greater than 1 ms, proper IC functionality cannot be guaranteed.

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TABLE 2. MAGNETIC SPECIFICATIONS					
CHARACTERISTIC	CONDITION	MIN.	TYP.	MAX.	UNIT
Back bias range	South pole facing back of package	500	-	4000	Gauss
Peak magnetic field	for linear operation	_	-	4500	Gauss
Switching differential	25°C	_	5	-	Gauss
Minimum signal lockout	peak to valley difference	_	38.5	_	Gauss
Magnetic signal	continuous operation (recommended)	75	-	-	Gauss
Output signal frequency	_	0	27.5	10000	Hz
Die placement tolerance	X and Y	-0.07	_	0.07	mm

TABLE 3. ENVIRONMENTAL SPECIFICATIONS					
CHARACTERISTIC	CONDITION	MIN.	TYP.	MAX.	UNIT
ESD (HBM)	JEDEC JS-001	8	_	_	kV

TABLE 4. ABSOLUTE MAXIMUM RATINGS					
CHARACTERISTIC	CONDITION	MIN.	TYP.	MAX.	UNIT
Operating temperature	ambient	-40	-	150	°C
Storage temperature	ambient, unbiased	-55	_	150	°C
Soldering temperature	applied for <10 s	_	_	265	°C
Output current (sinking)	open collector	_	_	20	mA
Forward supply voltage	dc voltage for 1 hour	_	_	26	V
Reverse supply voltage	dc voltage for 1 hour	-26	-	_	V

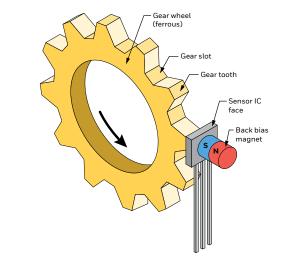
### NOTICE

Absolute maximum ratings are the extreme limits the device will momentarily withstand without damage to the device. Electrical and mechanical characteristics are not guaranteed if the rated voltage and/or currents are exceeded, nor will the device necessarily operate at absolute maximum ratings.

### ELECTROSTATIC SENSITIVE DEVICES DO NOT OPEN OR HANDLE EXCEPT AT A STATIC FREE WORKSTATION ESD SENSITIVITY: CLASS 3

#### SENSOR IC MOUNTING

The recommended sensor IC mounting position is in front of a gear wheel (see Figure 1). The south pole of the back bias magnet must face the back of the sensor IC package. The sensing distance is measured between the sensor IC front and the tip of the gear tooth. The sensor IC senses ferrous targets. The sensor IC contains a single Hall element and a symmetrical magnet such that the sensor IC can be rotated on its axis with no performance degradation. The input voltage is constantly compared to the voltage held at the output of the peak hold circuit. Whenever the input is higher than the peak hold value, the comparator triggers the counter to increment. This continues until the input value no longer exceeds the peak hold value.



**FIGURE 1. SENSOR IC MOUNTING** 

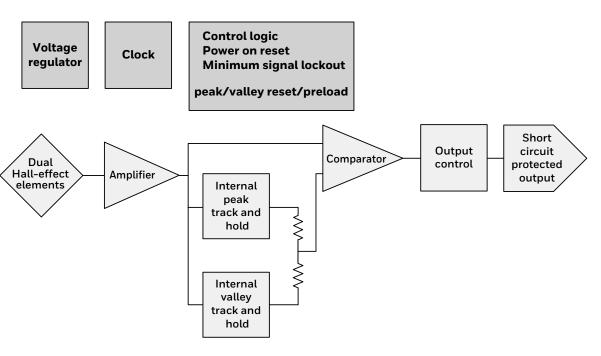
## VG481V1 Back-Biased Hall-effect Speed Sensor IC

### NOTICE

Observe the following precautions when using magnetometers in general:

- The presence of ferrous materials, such as nickel iron, steel and cobalt near the sensor will create disturbances in the earth's magnetic field that will distort x, y, z axis measurements. Non-ferrous materials such as copper brass, gold, aluminum, some stainless steel, silver, tin silicon, and any nonmetallic material will not distort the field measurements.
- The presence of the earth's magnetic filed must be taken into account when measuring the x, y, z fields and it varies considerably in different parts of the world. Differences in the earth's magnetic filed are quite dramatic between North America, South America, and the equator.
- Typical targets are the 120 mm to 300 mm in diameter. The tooth shapes vary (square, triangular, windows). The teeth are of similar size, equally spaced, and with no signature features. The sensing distance depends on the tooth/slot spacing and frequency due to eddy current effects.

#### FIGURE 2. HIGH LEVEL CIRCUIT BLOCK DIAGRAM





#### **TYPICAL OPERATION**

Figure 2 shows the circuit's high level block diagram. During normal operation, peak maximum and valley minimum values, proportional to the magnetic signal, are tracked and held. The average of these two values is used to produce a slicing level that is compared to the input signal to produce the output transitions.

Upon transition from output low to high state, the valley minimum value is preloaded in the positive direction, an amount equal to the difference of the previous peak and valley divided by four. Likewise, upon transition from output high to low state, the peak maximum is preloaded in the negative direction in an amount equal to the difference of the previous peak and valley divided by four (see Figure 3).

The preload values are calculated from the peak and valley counter register values. This preload function allows the sensor to track targets with run-out and other variations.

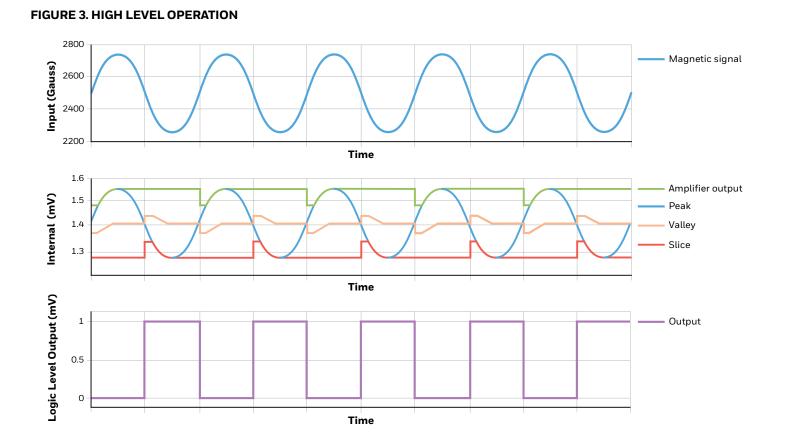
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#### FIGURE 5. INTERNAL PEAK SAMPLE AND HOLD OPERATION



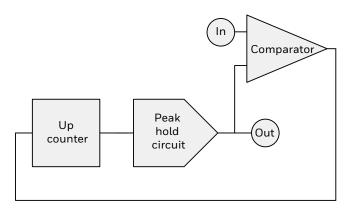
The internal valley track and hold circuit works identically, except in the opposite direction. The peak hold and valley hold

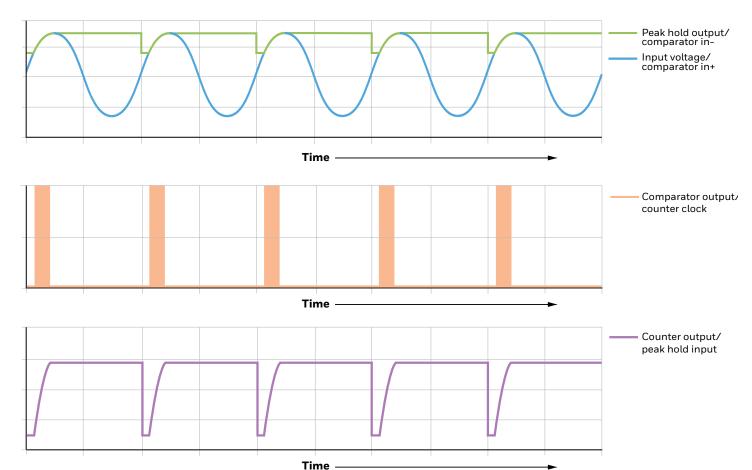
outputs are averaged to produce the slicing level

#### **INTERNAL TRACK AND HOLD OPERATION (See Figures 4, 5)**

The input voltage is constantly compared to the voltage held at the output of the peak hold circuit. Whenever the input is higher than the peak hold value, the comparator triggers the counter to increment. This continues until the input value no longer exceeds the peak hold value.

FIGURE 4. INTERNAL PEAK TRACK AND HOLD BLOCK DIAGRAM





**POWER ON RESET** 

At power up, and any time the supply voltage drops below a level that allows reliable operation, the sensor IC ceases operation and will not provide an output until the supply voltage rises to a proper level. The output will be high until the power on reset releases, and it will remain high until the first valid high-to-low transition.

#### MINIMUM SIGNAL LOCKOUT

Sensor IC output is enabled when the peak-to-valley difference is greater than 38.5 Gauss typical. When the peak-to-valley difference degrades to less than 27.5 Gauss typical, the sensor IC output is disabled.



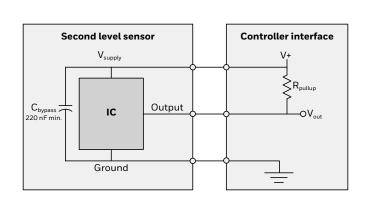
#### **STARTUP SEQUENCE**

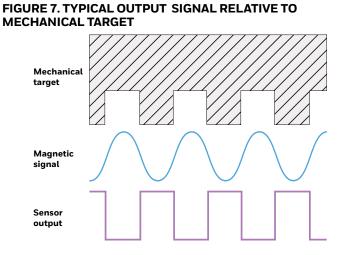
Sensor IC startup consists of three stages:

- 1. Power on reset circuit release: This occurs once sufficient stable operating voltage is applied.
- 2. Internal peak and valley track and hold circuits begin to fully track the input signal: This is typically less than 1 ms.
  - 3. Minimum signal lockout release: This occurs when the peakto-valley difference is greater than 38.5 Gauss.
- After the three startup stages, the sensor IC output will switch normally.

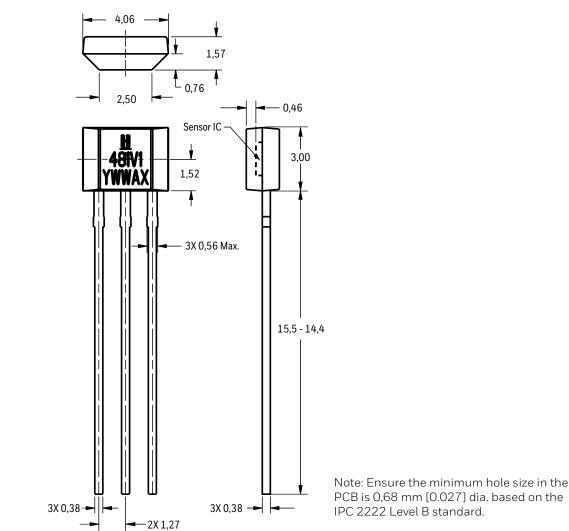
### VG481V1 Back-Biased Hall-effect Speed Sensor IC

#### **FIGURE 6. MINIMUM CIRCUIT FOR OPERATION**





#### FIGURE 8. PACKAGE DIMENSIONS (FOR REFERENCE ONLY: MM/[IN])



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### **PERSONAL INJURY**

DO NOT USE these products as safety or emergency stop devices or in any other application where failure of the product could result in personal injury.

#### Failure to comply with these instructions could result in death or serious injury.

#### Warranty/Remedy

Honeywell warrants goods of its manufacture as being free of defective materials and faulty workmanship during the applicable warranty period. Honeywell's standard product warranty applies unless agreed to otherwise by Honeywell in writing; please refer to your order acknowledgement or consult your local sales office for specific warranty details. If warranted goods are returned to Honeywell during the period of coverage, Honeywell will repair or replace, at its option, without charge those items that Honeywell, in its sole discretion, finds defective. The foregoing is buyer's sole remedy and is in lieu of all other warranties, expressed or implied, including those of

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Specifications may change without notice. The information we supply is believed to be accurate and reliable as of this writing. However, Honeywell assumes no responsibility for its use.

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